



CMOS 16-BIT 3.3V TO 5V LEVEL SHIFTING TRANSCEIVER WITH 3-STATE OUTPUTS

IDT74ALVC164245 ADVANCE INFORMATION

FEATURES:

- 0.5 MICRON CMOS Technology
- Typical $t_{sk(0)}$ (Output Skew) < 250ps
- ESD > 2000V per MIL-STD-883, Method 3015; > 200V using machine model (C = 200pF, R = 0)
- 0.635mm pitch SSOP, 0.50mm pitch TSSOP, and 0.40mm pitch TVSOP packages
- Extended commercial range of - 40°C to + 85°C
- $V_{CCA} = 2.7V$ to 3.6V
- $V_{CCB} = 5V \pm 0.5V$
- CMOS power levels (0.4μW typ. static)
- Rail-to-Rail output swing for increased noise margin

Drive Features for ALVC164245:

- High Output Drivers: $\pm 24mA$
- Suitable for heavy loads

DESCRIPTION:

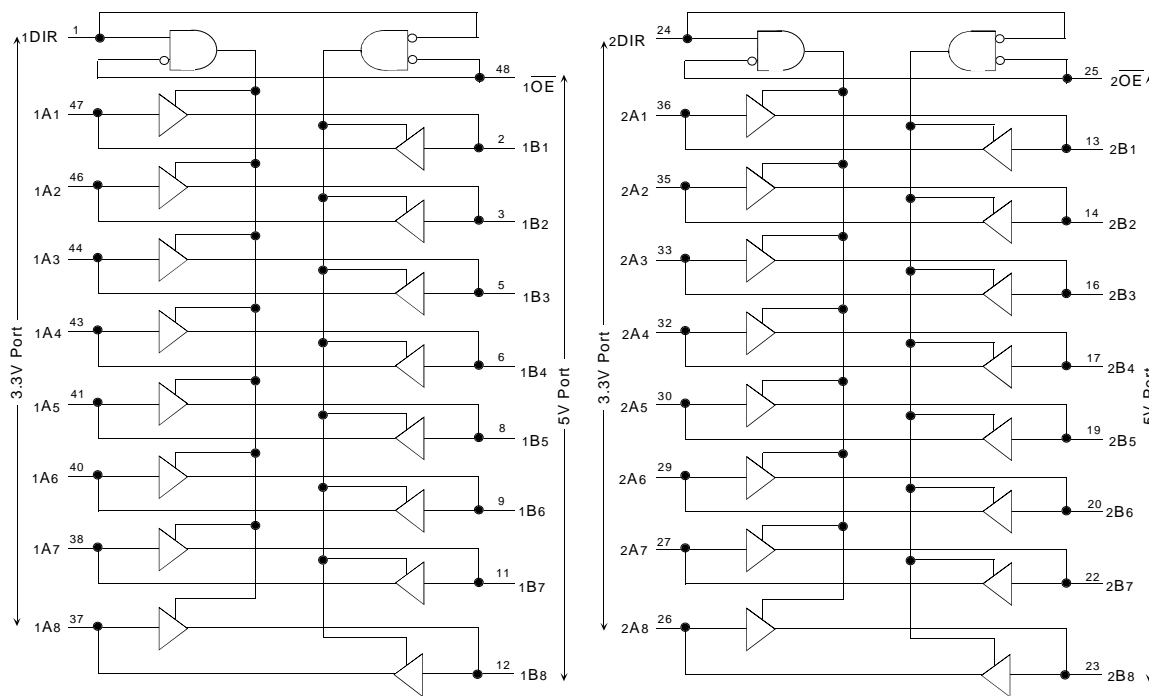
This 16-bit 3.3V to 5V level shifting transceiver is manufactured using advanced dual metal CMOS technology. The ALVC164245 contains two separate supply rails; B port has V_{CCB} , which is set at 5V, and A port has V_{CCA} , which is set to operate at 3.3V. This allows for translation from a 3.3V to 5V environment and vice-versa. This device is designed for asynchronous communication between data buses.

The ALVC164245 has been designed with a $\pm 24mA$ output driver. This driver is capable of driving a moderate to heavy load while maintaining speed performance.

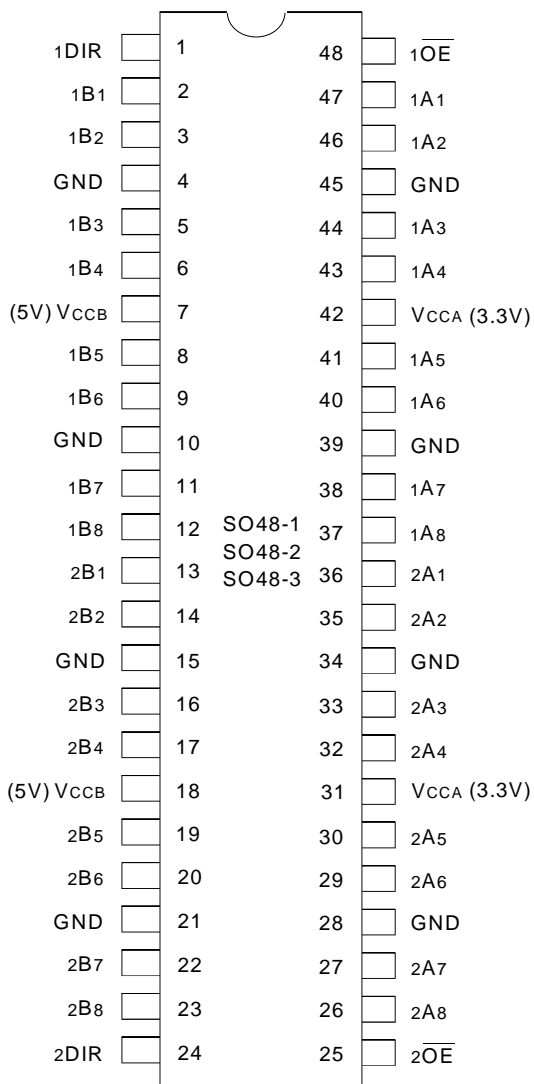
APPLICATIONS:

- Mixed 3.3V and 5V High Speed Systems
- 5V PCI Interface to 3.3V PC Bus Structures
- Telecommunication Legacy Systems with transitions from 5V to 3.3V

Functional Block Diagram



PIN CONFIGURATION



SSOP/TSSOP/
TVSOP TOP VIEW

ABSOLUTE MAXIMUM RATING FOR VCCB⁽¹⁾

Symbol	Description	Max.	Unit
V _{TERM} ⁽²⁾	Terminal Voltage with Respect to GND	- 0.5 to + 6	V
V _{TERM} ⁽³⁾	Terminal Voltage with Respect to GND	- 0.5 to + 6	V
T _{STG}	Storage Temperature	- 65 to + 150	°C
I _{OUT}	DC Output Current	- 50 to + 50	mA
I _{IK}	Continuous Clamp Current, V _I < 0 or V _I > V _{CC}	± 50	mA
I _{OK}	Continuous Clamp Current, V _O < 0	- 50	mA
I _{CC} I _{SS}	Continuous Current through each V _{CC} or GND	± 100	mA

ABSOLUTE MAXIMUM RATING FOR VCCA⁽¹⁾

Symbol	Description	Max.	Unit
V _{TERM} ⁽²⁾	Terminal Voltage with Respect to GND	- 0.5 to + 6	V
V _{TERM} ⁽³⁾	Terminal Voltage with Respect to GND	- 0.5 to + 6	V
T _{STG}	Storage Temperature	- 65 to + 150	°C
I _{OUT}	DC Output Current	- 50 to + 50	mA
I _{IK}	Continuous Clamp Current, V _I < 0 or V _I > V _{CC}	± 50	mA
I _{OK}	Continuous Clamp Current, V _O < 0	- 50	mA
I _{CC} I _{SS}	Continuous Current through each V _{CC} or GND	± 100	mA

NOTES:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- V_{CC} terminals.
- All terminals except V_{CC}.

PIN DESCRIPTION

Pin Names	Description
\overline{xOE}	Output Enable Inputs (Active LOW)
xDIR	Direction Control Inputs
xAx	Port A Inputs or 3-State Outputs
xBx	Port B Inputs or 3-State Outputs

FUNCTION TABLE (each 8-bit section)⁽¹⁾

Inputs		Outputs
\overline{xOE}	xDIR	
L	L	Bus B Data to Bus A
L	H	Bus A Data to Bus B
H	X	High Z State

NOTE:

- H = HIGH Voltage Level
L = LOW Voltage Level
X = Don't Care
Z = High-Impedance

CAPACITANCE (TA = +25°C, F = 1.0MHZ, VCCB = 5V)

Symbol	Parameter ⁽¹⁾	Conditions	Typ.	Max.	Unit
C _{IN}	B port Input Capacitance	V _{IN} = 0V	6.5	—	pF
C _{I/O}	B port I/O Capacitance	V _{IN} = 0V	6.5	—	pF

CAPACITANCE (TA = +25°C, F = 1.0MHZ, VCCA = 3.3V)

Symbol	Parameter ⁽¹⁾	Conditions	Typ.	Max.	Unit
C _{IN}	A port Input Capacitance	V _{IN} = 0V	6.5	—	pF
C _{I/O}	A port I/O Capacitance	V _{IN} = 0V	8.5	—	pF

NOTE:

- As applicable to the device type.

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE (B PORT)

Following Conditions Apply Unless Otherwise Specified:

Operating Condition: TA = -40°C to +85°C

Symbol	Parameter	Test Conditions		Min.	Typ. ⁽²⁾	Max.	Unit
V _{IH}	Input HIGH Voltage Level	V _{CCB} = 4.5V to 5.5V		2	—	—	V
V _{IL}	Input LOW Voltage Level	V _{CCB} = 4.5V to 5.5V		—	—	0.8	V
I _{IH}	Input HIGH Current	V _{CCB} = 5.5V	V _I = V _{CC}	—	—	± 5	μA
I _{IL}	Input LOW Current	V _{CCB} = 5.5V	V _I = GND	—	—	± 5	
I _{OZH}	High Impedance Output Current (3-State Output pins)	V _{CCB} = 5.5V	V _O = V _{CC}	—	—	± 10	μA
I _{OZL}			V _O = GND	—	—	± 10	μA
V _H	Input Hysteresis	V _{CCB} = 4.5V		—	100	—	mV
I _{CCL}	Quiescent Power Supply Current	V _{CCB} = 5.5V		—	0.1	40	μA
I _{CCH}		V _{IN} = GND or V _{CCB}					
I _{CCZ}							
ΔI _{CC}	Quiescent Power Supply Current Variation	One input at 3.4V, other inputs at V _{CCB} or GND		—	—	750	μA

NOTES:

- V_{CCA} = 2.7V to 3.6V
- Typical values are at V_{CC} = 3.3V, +25°C ambient.

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE (A PORT)^(1, 2)

Following Conditions Apply Unless Otherwise Specified:

Operating Condition: TA = -40°C to +85°C

Symbol	Parameter	Test Conditions		Min.	Typ. ⁽³⁾	Max.	Unit
V _{IH}	Input HIGH Voltage Level	V _{CCA} = 2.7V to 3.6V		2	—	—	V
V _{IL}	Input LOW Voltage Level	V _{CCA} = 2.7V to 3.6V		—	—	0.8	V
I _{IH}	Input HIGH Current	V _{CCA} = 3.6V	V _I = V _{CC}	—	—	± 5	μA
I _{IL}	Input LOW Current	V _{CCA} = 3.6V	V _I = GND	—	—	± 5	
I _{OZH}	High Impedance Output Current (3-State Output pins)	V _{CCA} = 3.6V	V _O = V _{CC}	—	—	± 10	μA
I _{OZL}			V _O = GND	—	—	± 10	μA
V _H	Input Hysteresis	V _{CCA} = 3.3V		—	100	—	mV
I _{CCL}	Quiescent Power Supply Current	V _{CCA} = 3.6V		—	0.1	40	μA
I _{CCH}		V _{IN} = GND or V _{CCA}					
I _{CCZ}							
ΔI _{CC}	Quiescent Power Supply Current Variation	One input at V _{CCA} - 0.6V, other inputs at V _{CCA} or GND		—	—	750	μA

NOTES:

- V_{CCB} = 5V ±0.5V.
- Control inputs xDIR, $\overline{\text{CE}}$ are supplied from V_{CCA}.
- Typical values are at V_{CC} = 3.3V, +25°C ambient.

OUTPUT DRIVE CHARACTERISTICS, VCCB = 5V ± 0.5V (B PORT)

Symbol	Parameter	Test Conditions ⁽¹⁾		Min.	Max.	Unit
VOH	Output HIGH Voltage (A port to B port)	VCCB = 4.5V	IOH = - 0.1mA	4.3	—	V
		VCCB = 5.5V		5.3	—	
		VCCB = 4.5V	IOH = - 24mA	3.7	—	
		VCCB = 5.5V		4.7	—	
VOL	Output LOW Voltage (A port to B port)	VCCB = 4.5V	IOL = 0.1mA	—	0.2	V
		VCCB = 5.5V		—	0.2	
		VCCB = 4.5V	IOL = 24mA	—	0.55	
		VCCB = 5.5V		—	0.55	

NOTE:

1. VIH and VIL must be within the min. or max. range shown in the DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE table for the appropriate Vcc range. TA = - 40°C to + 85°C; VCCA = 3.3V ± 0.3V.

OUTPUT DRIVE CHARACTERISTICS, VCCA = 3.3V ± 0.3V (A PORT)

Symbol	Parameter	Test Conditions ⁽¹⁾		Min.	Max.	Unit
VOH	Output HIGH Voltage (B port to A port)	VCCA = 2.7V to 3.6V	IOH = - 0.1mA	VCCA - 0.2	—	V
		VCCA = 2.7V		IOH = - 12mA	2.2	
		VCCA = 3V	IOH = - 24mA		2.4	
		VCCA = 3V		2	—	
VOL	Output LOW Voltage (B port to A port)	VCCA = 2.7V to 3.6V	IOL = 0.1mA	—	0.2	V
		VCCA = 2.7V		IOL = 12mA	—	
		VCCA = 3V	IOL = 24mA	—	0.55	

NOTE:

1. VIH and VIL must be within the min. or max. range shown in the DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE table for the appropriate Vcc range. TA = - 40°C to + 85°C; VCCB = 5V ± 0.5V.

OPERATING CHARACTERISTICS, $T_A = 25^\circ\text{C}$

Symbol	Parameter	Test Conditions	$V_{CCA} = 3.3\text{V}, V_{CCB} = 5\text{V}$	Unit
			Typical	
CPD	Power Dissipation Capacitance Outputs enabled (A port or B port)	$C_L = 0\text{pF}, f = 10\text{MHz}$	56	pF
CPD	Power Dissipation Capacitance Outputs disabled (A port or B port)		6	pF

SWITCHING CHARACTERISTICS⁽¹⁾

Symbol	Parameter	$V_{CCB} = 5\text{V} \pm 0.5\text{V}$				Unit
		$V_{CCA} = 2.7\text{V}$		$V_{CCA} = 3.3\text{V} \pm 0.3\text{V}$		
		Min.	Max.	Min.	Max.	
t _{PLH} t _{PHL}	Propagation Delay xAx to xBx	—	5.9	1	5.8	ns
t _{PLH} t _{PHL}	Propagation Delay xBx to xAx	—	6.7	1.2	5.8	ns
t _{PZH} t _{PZL}	Output Enable Time x $\overline{\text{OE}}$ to xBx	—	9.3	1	8.9	ns
t _{PZH} t _{PZL}	Output Enable Time x $\overline{\text{OE}}$ to xAx	—	10.2	2	9.1	ns
t _{PHZ} t _{PLZ}	Output Disable Time x $\overline{\text{OE}}$ to xBx	—	9.2	2.1	9.5	ns
t _{PHZ} t _{PLZ}	Output Disable Time x $\overline{\text{OE}}$ to xAx	—	9	2.9	8.6	ns

NOTE:

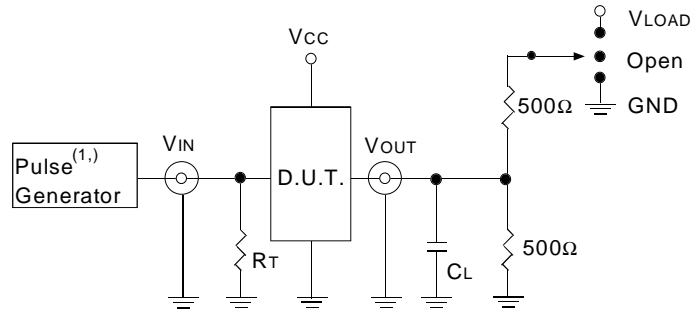
1. See test circuits and waveforms. $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$.

TEST CIRCUITS AND WAVEFORMS FOR $V_{CCA} = 3.3V \pm 0.3V$ AND $V_{CCA} = 2.7V$

TEST CONDITIONS

Symbol	$V_{CCA} = 3.3V \pm 0.3V$	$V_{CCA} = 2.7V$	Unit
V_{LOAD}	6	6	V
V_{IH}	3	3	V
V_T	1.5	1.5	V
V_{LZ}	300	300	mV
$V_{OH} - V_{HZ}$	300	300	mV
C_L	50	50	pF

TEST CIRCUITS FOR ALL OUTPUTS



DEFINITIONS:

C_L = Load capacitance: includes jig and probe capacitance.

R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator.

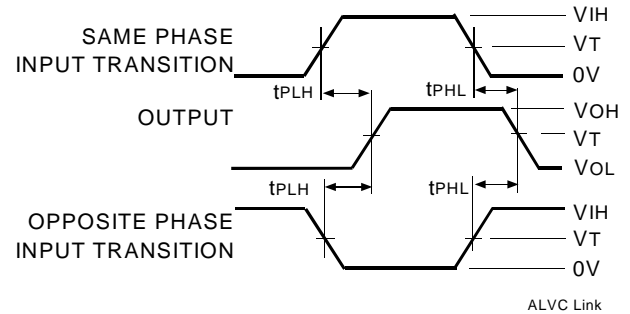
NOTE:

1. Pulse Generator for All Pulses: Rate $\leq 10\text{MHz}$; $t_f \leq 2.5\text{ns}$; $t_r \leq 2.5\text{ns}$.

SWITCH POSITION

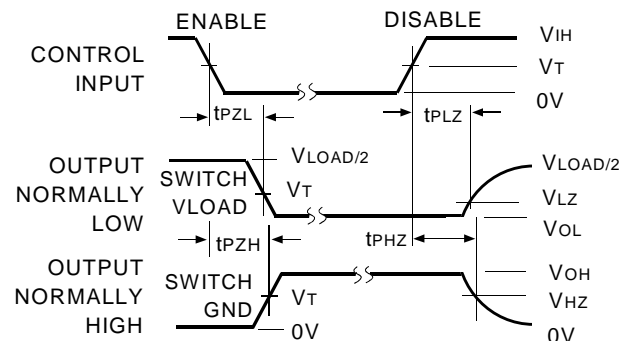
Test	Switch
Disable Low Enable Low	V_{LOAD}
Disable High Enable High	GND
All Other tests	Open

PROPAGATION DELAY



ALVC Link

ENABLE AND DISABLE TIMES



NOTE:

1. Diagram shown for input Control Enable-LOW and input Control Disable-HIGH.

ALVC Link

TEST CIRCUITS AND WAVEFORMS FOR $V_{CCB} = 5V \pm 0.5V$:

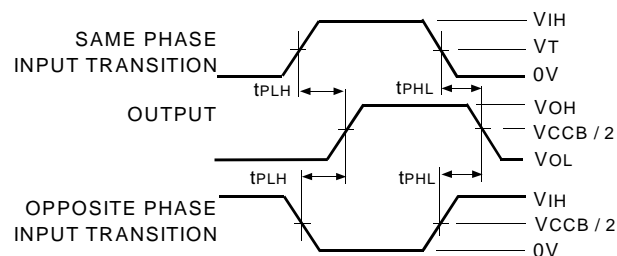
TEST CONDITIONS (USE V_{CCA} TEST CIRCUIT)

Symbol	$V_{CCB}^{(1)} = 5V \pm 0.5V$	Unit
V_{LOAD}	$2 \times V_{CCB}$	V
V_{IH}	2.7	V
V_T	1.5 or $V_{CCB} / 2$	V
V_{LZ}	20% of V_{CCB}	mV
V_{HZ}	80% of V_{CCB}	mV
C_L	50	pF

NOTE:

1. Pulse Generator for All Pulses: Rate $\leq 10\text{MHz}$; $t_f \leq 2.5\text{ns}$; $t_r \leq 2.5\text{ns}$.

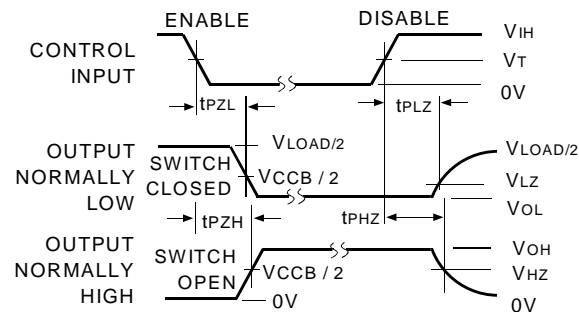
PROPAGATION DELAY



NOTES:

1. For $t_{sk(o)}$ OUTPUT1 and OUTPUT2 are any two outputs.
2. For $t_{sk(b)}$ OUTPUT1 and OUTPUT2 are in the same bank.

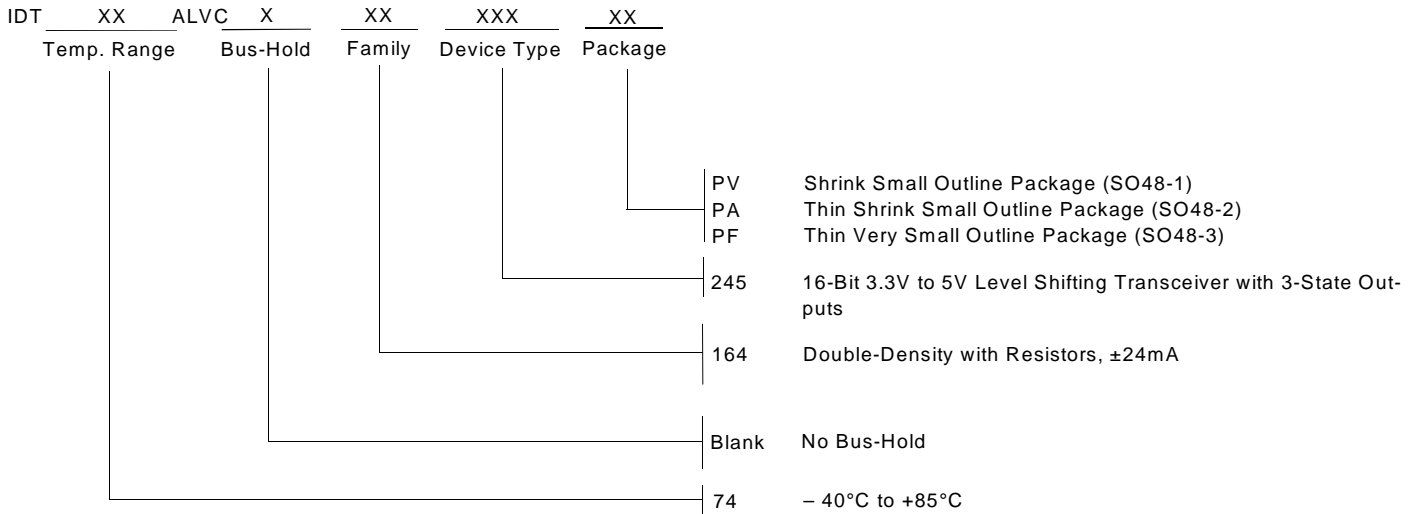
ENABLE AND DISABLE TIMES



NOTE:

1. Diagram shown for input Control Enable-LOW and input Control Disable-HIGH.

ORDERING INFORMATION



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